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NOTICE OF ALLOWANCE AND FEE(S) DUE

32361 7590 07/22/2009

GREENBERG TRAURIG, LLP
MET LIFE BUILDING
200 PARK AVENUE
NEW YORK, NY 10166

EXAMINER	
SIM, YONG H	
ART UNIT	PAPER NUMBER
2629	

DATE MAILED: 07/22/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/506,467	08/11/2005	Yingjian Liu	58260-011300	8948

TITLE OF INVENTION: WIRELESS AND PASSIVE TABLET FOR INPUTTING TO COMPUTER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$755	\$300	\$0	\$1055	10/22/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

32361 7590 07/22/2009

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200 PARK AVENUE
NEW YORK, NY 10166

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)

(Signature)

(Date)

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nonprovisional	YES	\$755	\$300	\$0	\$1055	10/22/2009
EXAMINER	ART UNIT	CLASS-SUBCLASS				
SIM, YONG H	2629	345-173000				
1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).			2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.			1 _____ 2 _____ 3 _____
3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)						

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:

- Issue Fee
- Publication Fee (No small entity discount permitted)
- Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)

- A check is enclosed.
- Payment by credit card. Form PTO-2038 is attached.
- The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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GREENBERG TRAURIG, LLP MET LIFE BUILDING 200 PARK AVENUE NEW YORK, NY 10166				SIM, YONG H
ART UNIT		PAPER NUMBER		
2629				DATE MAILED: 07/22/2009

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 112 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 112 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability	Application No.	Applicant(s)	
	10/506,467	LIU ET AL.	
	Examiner	Art Unit	
	YONG SIM	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 7/13/2009.
2. The allowed claim(s) is/are 1,2 and 4-8.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

DETAILED ACTION

Allowable Subject Matter

1. Claims 1 – 2 and 4 – 8 are allowed.
2. The following is an examiner's statement of reasons for allowance:

Lee et al. (US 6,476,799 B1) teach an apparatus provided for the wireless capture of coordinate-shift information. The apparatus includes a pulse generation that generates a pulse signal and simultaneously distributes the pulse signal into a first signal along the first path and a second signal along the second path.

Oda (US 5,646,377) teaches a method for providing coordinate information which is indicated by a pointing device. The point detecting device transmits signals from both ends of the loop coil arrangement towards the pointing device, and/or receives at both ends the signals generated by the electromagnetic function between the loop coil arrangements and pointing device.

Wieczorek teaches a cordless position detection apparatus comprising a plurality of coils running along the x-direction and y-directions and continuously generates a square wave pulses wherein the frequency of a resonant signal produced from a pen circuit is different from the frequency of the transmitted electromagnetic waves

But none of the prior art of reference teaches a (RX+) terminal for receiving signals connects with ends of first and second parallel resistors (R1 and R2), the other end of the second resistor (R2) connects with a negative input pin (2) of a first amplifier (IC12A) and one end of a third resistor (R3) in parallel, and the other end of the third resistor (R3) connects with an output pin (1) of the first amplifier (IC12A), a first pin (12) of an amplifying circuit chip (IC 14) and one end of a fourth resistor (R6); a grounding pin (4) of the first amplifier (IC 12A) is connected to analogue ground; the other end of the first resistor (R1) connects with one end of a fifth resistor (R4), a first capacitor (C6) and a reference voltage terminal; the other end of the fifth resistor (R4) connects with a positive input pin (3) of the first amplifier (IC 12A), the other end of the first capacitor (C6) is connected to analogue ground and one end of a second capacitor (C7), the other end of the second capacitor (C7) connects with a power supply connection pin (8) of the first amplifier (IC 12A) and a first power supply (VDD); the other end of the fourth resistor (R6) connects with a second pin (13) of said chip (IC14) and one end of a sixth resistor (R7); the other end of the sixth resistor (R7) connects with a third pin 14 of said chip (IC14) and one end of a seventh resistor (R8); the other end of the seventh resistor (R8) connects with a fourth pin (15) of said chip (IC 14) and one end of an eighth resistor (R9); the other end of the eighth resistor (R9) connects with a fifth pin (1) of said chip (IC14) and one end of a ninth resistor (R10); the other end of the ninth resistor (R10) connects with a sixth pin (2) of said chip (IC 14) and one end of a tenth resistor (R11); the other end of the

tenth resistor (R11) connects with a seventh pin (4) of said chip (IC14) and one end of an eleventh resistor (R12); the other end of the eleventh resistor (R12) connects with an eighth pin (5) of said chip (IC14) and one end of a twelfth resistor (R13); the other end of the twelfth resistor (R13) connects with a reference voltage terminal; a ninth pin (3) of said chip (IC 14) connects with one end of a third capacitor (C 1), the other end of the third capacitor (C 1) connects with one end of a thirteenth resistor (R16) and a positive input pin (5) of a second amplifier (IC12B); the other end of the thirteenth resistor (R16) connects with the reference voltage terminal; an output pin (7) of the second amplifier IC 12B, which outputs the output signals, connects with one end of a fourteenth resistor (R26); the other end of the fourteenth resistor (R26) connects with a negative input pin (6) of the second amplifier (IC12B) and one end of a fifteenth resistor (R23); the other end of the fifteenth resistor (R23) connects with the reference voltage terminal; a tenth pin (11) of said chip (IC14) connects with a first signal connection (GA), and an eleventh pin (10) of said chip (IC14) connects with a second signal connection (GB) and a twelfth pin (9) of said chip (IC 14) connects with a third signal connection (GC), and a thirteenth pin (16) of said chip (IC 14) connects with the first power supply (VDD) and one end of a fourth capacitor (C 14); the other end of the fourth capacitor (C 14) connects with the analogue ground, and a fourteenth pin (6), a fifteenth pin (7) and a sixteenth pin (8) of said chip (IC 14) connect with analogue ground.

Therefore, the claims are allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

/Amr Awad/
Supervisory Patent Examiner, Art Unit 2629